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By: Madhus Nall

Date: April 30, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Thomas Huttner et al.  
Applic. No. : 09/313,424 ✓  
Filed : May 17, 1999  
Title : SOI Semiconductor Configuration And Method Of  
Fabricating The Same  
Examiner : Brook Kebede  
Group Art Unit : 2823

A M E N D M E N T

Hon. Commissioner of Patents and Trademarks,  
Washington, D. C. 20231

S i r :

Responsive to the non-final Office action dated January 30,  
2003, kindly amend the above-identified application as  
follows:

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